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DK Parker, ER Swenson, MM Yip, CJ Young - US Patent App. 10/814,725, 2004 - Google Patents ... San Jose, CA (US); Michael M. Yip, Los Altos, CA (US); Christopher J. Young, San Jose, CA (US) Correspondence Address: HOWREY LLP C/0 IP DOCKETING DEPARTMENT ...

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# Pipelined packet processor

DK Parker - US Patent App. 10/835,598, 2004 - Google Patents
Page 1. US 20050226242A1 (19) United States (12) Patent Application Publication
(io> Pub. NO.: US 2005/0226242 Al Parker (43) Pub. Date: Oct. ...

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# System and method for controlling routing in a virtual router system

N Alam - US Patent 7,340,535, 2008 - freepatentsonline.com ... Data link layer objects include VLAN or other such data link layer objects, and physical layer objects include ATM, DS3, or ... 23:16, TOS/EXP, IP TOS/MPLS EXP ... Related articles - Web Search - All 3 versions

Apparatus and method for interworking between MPLS network and non-MPLS network K Wakayama, K Sakamoto, T Aimoto, T Miyamoto - US Patent App. 11/245,332, 2005 - Google Patents ... TPID TCI IP HEADER IP PAYLOAD — -} — FCS 500 USER PRIORITY CFI VLAN ID (t ... S ((i PPP HEADER SHIM HEADER IP 1 7 HEADER IP PAYLOAD PCS LABEL EXP S TTL ...

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A practical architecture for implementing end-to-end QoS in an IPnetwork - [PDF] ► SKKU.ac.kr V Fineberg - Communications Magazine, IEEE, 2002 - leeexplore.leee.org ... L3 = IP: • IETF - RSVP • IETF - ToS / DiffServ • IETF - IPSec L2 = Ethernet: • IEEE - 802.3x = full-duplex flow-control • IEEE - 802.1Q = VLAN tags ... Cited by 62 - Related articles - Web Search - BL Direct - All 8 versions

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钟锦 台 - 信息 网络, 2004 - cqvip.com

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ER Swenson, CJ Young - US Patent 7,304,996, 2007 - Google Patents Page 1. (12) United States Patent Swenson et al. (54) SYSTEM AND METHOD FOR ASSEMBLING ADATA PACKET (75) Inventors: Erik R. Swenson ...

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